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Docket No.: 070456-0105

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of

Tatsuo HIRAMATSU, et al.

Application No.: 10/573,970

Filed: March 30, 2006

: Customer Number: 20277

: Confirmation Number: 1749

: Group Art Unit: 2111

: Examiner: Not yet assigned

For: PROCESSING DEVICE WITH RECONFIGURABLE CIRCUIT, INTEGRATED CIRCUIT
DEVICE AND PROCESSING METHOD USING THESE DEVICES

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the reference listed on the attached form PTO-1449. It is respectfully requested that the reference be expressly considered during the prosecution of this application, and that the reference be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The reference was first cited in a corresponding foreign application office action and its relevance discussed therein. Please note that the other two cited references, JP 2001-68993 and Herman SCHMIT "Incremental Reconfiguration for Pipelined Applications" were previously

submitted to the United States Patent and Trademark Office on March 30, 2006. A copy of the foreign office action, together with an English language version thereof, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**Please recognize our Customer No. 20277
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SHEET 1 OF 1

INFORMATION DISCLOSURE
CITATION IN AN
APPLICATION

(PTO-1449)

ATTY. DOCKET NO.
070456-0105SERIAL NO.
10/573,970APPLICANT
Tatsuo HIRAMATSU, et al.FILING DATE
March 30, 2006GROUP
2111

U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US			
		US			
		US			
		US			
		US			
		US			
		US			

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number 4-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
		JP 2000-181566	06/30/2000	MITSUBISHI ELECTRIC CORP; MITSUBISHI ELECTRIC ENGINEERING CO LTD		JAPAN (w/ English Abstract)

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		Japanese Office Action for Corresponding Application No. 2003-362216 filed Mailed January 16, 2007

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.